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## » Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐ 1. **A 9-M tr. access network system-on-a-chip for mega-bit Internet access at home**  
 Kozu, S.; Aramaki, T.; Ikeda, C.; Kuroda, Y.; Kawanago, S.; Okada, M.; Kariya, H.; Manabe, M.; Utani, H.; Sudou, E.; Oda, Y.; Suzuki, H.;  
 Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE 2000  
 21-24 May 2000 Page(s):231 - 234  
 Digital Object Identifier 10.1109/CICC.2000.852655  
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) IEEE CNF
- ☐ 2. **A single chip terminal solution for high-end telephone applications**  
 Nair, V.; Erdmann, M.; Mishra, S.; Povazanec, J.; Shaligram, A.; Hu Chun Feng;  
 Custom Integrated Circuits, 2001, IEEE Conference on.  
 6-9 May 2001 Page(s):417 - 420  
 Digital Object Identifier 10.1109/CICC.2001.929813  
[AbstractPlus](#) | Full Text: [PDF](#)(628 KB) IEEE CNF
- ☐ 3. **Implementation of the USB interface in the instrumentation for sound and vibration measurements and analysis**  
 Podgorski, A.; Nedwidek, R.; Pochmara, M.;  
 Intelligent Data Acquisition and Advanced Computing Systems: Technology and Applications, 2003. Proceedings of the  
 Second IEEE International Workshop on  
 2003 Page(s):159 - 163  
 Digital Object Identifier 10.1109/IDAACS.2003.1249539  
[AbstractPlus](#) | Full Text: [PDF](#)(481 KB) IEEE CNF
- ☐ 4. **A mixed-mode ASIC for interface control of smart-card parcmeter**  
 Compagne, E.; Ilie, F.;  
 Euro ASIC'91  
 27-31 May 1991 Page(s):124 - 127  
 Digital Object Identifier 10.1109/EUASIC.1991.212881  
[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) IEEE CNF


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Derwent World Patents Index

IBM Technical Disclosure Bulletins

Term:

20030093606 or 20030079077

Display:

10

Documents in Display Format:

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side by side		result set
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<u>L3</u> 20030093606 or 20030079077	2	<u>L3</u>
<u>L2</u> 20030145141	1	<u>L2</u>
DB=USPT; PLUR=YES; OP=OR		
<u>L1</u> 6567273.pn. or 5773332.pn. or 6438638.pn. or 6372974.pn.	4	<u>L1</u>

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## Refine Search

### Search Results -

Terms	Documents
L1 same (serial adj1 bus) same (host or CPU or computer)	21

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	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L2</u>	L1 same (serial adj1 bus) same (host or CPU or computer)	21	<u>L2</u>
<u>L1</u>	((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)	25887	<u>L1</u>

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L2	0

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<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L3</u>	L2	0	<u>L3</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L2</u>	L1 same (serial adj 1 bus) same (host or CPU or computer)	21	<u>L2</u>
<u>L1</u>	((dual adj 1 mode) or mode) near3 (microcontroller or (micro adj 1 controller) or controller)	25887	<u>L1</u>

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts

Pending

Active

L1: (17811) ((dual adj1

L2: (14) 11 same (serial

L3: (21) 11 same (serial

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

Let

Browse

Queue

Clear

DBs

USPAT

Default operator

OR

Plurals

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definit	Er
1	BRS	L1	17811	((dual adj1 mode) or mode) near3 (microcon	USPA	2005/09/21 17:16				
2	BRS	L2	14	11 same (serial adj1 bus) same (host or CP	USPA	2005/09/21 17:18				
3	BRS	L3	21	11 same (serial adj1 bus) same (host or CP	USPA	2005/09/21 17:18				

**EAST - [Untitled1:1]**

File View Edit Tools Window Help

☐ Drafts  
☐ Pending  
☒ Active  
     L1: (17811) ((dual adj1  
     L2: (14) 11 same (seria  
     L3: (21) 11 same (seria  
☐ Failed  
☐ Saved  
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Search      
 DBs: USPAT ☒ Plurals ☒ Highlight all hit terms initially  
 Default operator: OR

11 same (serial adj1 bus) same (host or CPU or computer or processor)

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6820160	20041116	13	Apparatus for optically isolating a USB periphe	710/305	710/36;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6813650	20041102	12	Multimode non-standard universal serial bus co	710/14	710/62;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6760333	20040706	44	Hybrid digital subscriber loop and voi	370/395.1	341/22;
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6643728	20031104	20	Method and apparatus for converting IEEE 128	710/315	345/173;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6567921	20030520	7	Asynchronous low power mode bus controller cir	713/322	370/465;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6513085	20030128	25	Link/transaction layer controller with integra	710/305	375/222;
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6505263	20030107	21	Bus controller operating code in svste	710/100	710/313;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6463362	20021008	7	Electronic device and operating mode control	700/295	710/107;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6266715	20010724	16	Universal serial bus controller with a direc	710/22	710/305;
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6209050	20010327	11	Expansion card	710/301	700/297;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6122694	20000919	8	Serial bus controller	710/110	700/298;